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Docket No.: M4065.0997/P997  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Chandra Mouli

Application No.: 10/754,565

Confirmation No.: 8237

Filed: January 12, 2004

Art Unit: 2811

For: USING HIGH-K DIELECTRICS IN  
ISOLATION STRUCTURES METHOD,  
PIXEL AND IMAGER DEVICE

Examiner: S. H. Y. Loke

**RESPONSE TO RESTRICTION REQUIREMENT**

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the restriction requirement set forth in the Office Action mailed April 5, 2005 (Paper No. Mail Date 20050404), Applicant hereby elects Group I, claims 1-42 for continued examination without traverse.

The Examiner has required restriction between the following two groups:

Group I, claims 1-42, drawn to a semiconductor device; and

Group II, claims 43-66, drawn to a method to make a semiconductor device.

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Dated: April 19, 2005

Respectfully submitted,

By 

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